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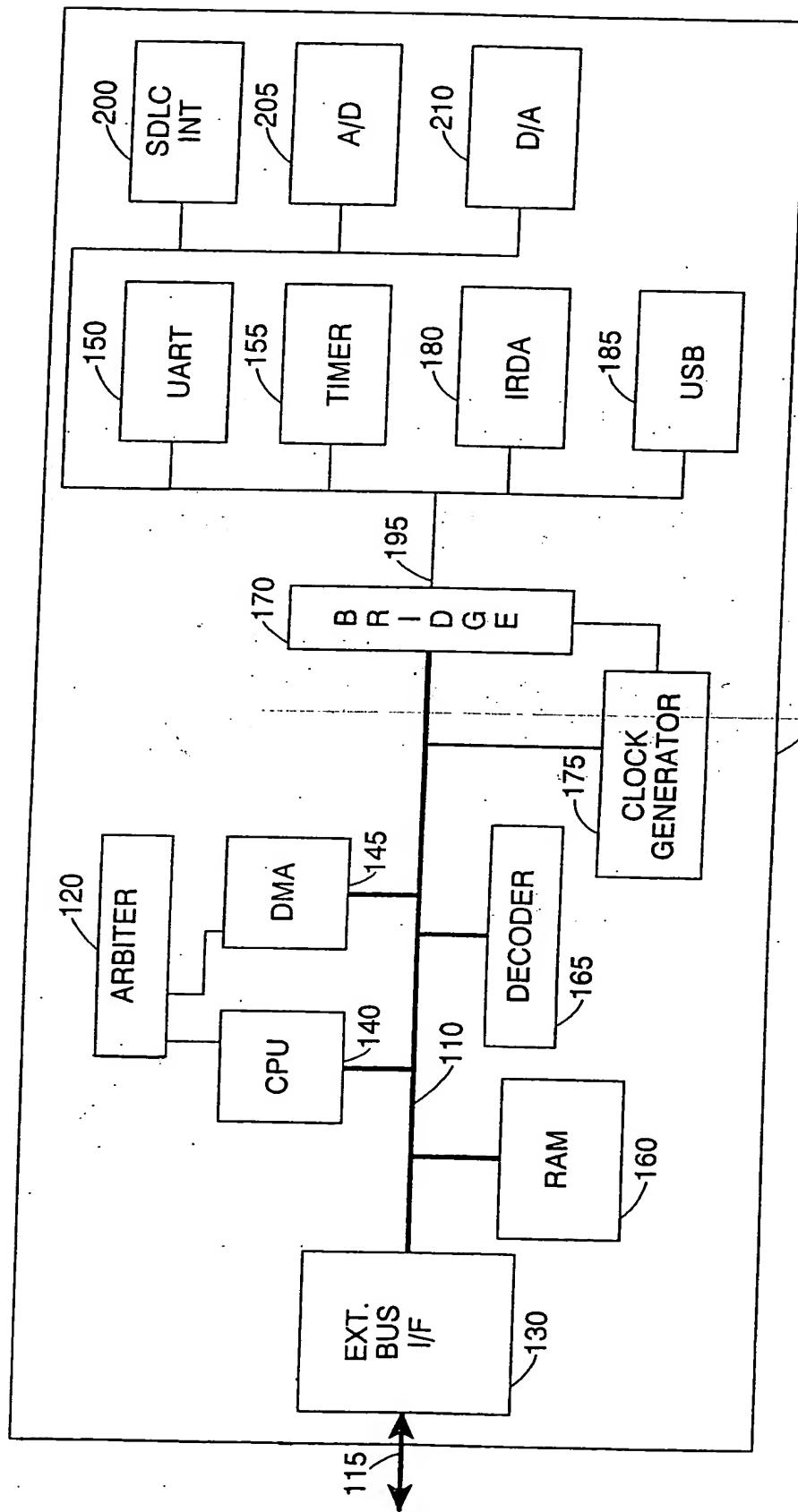


FIG. 1

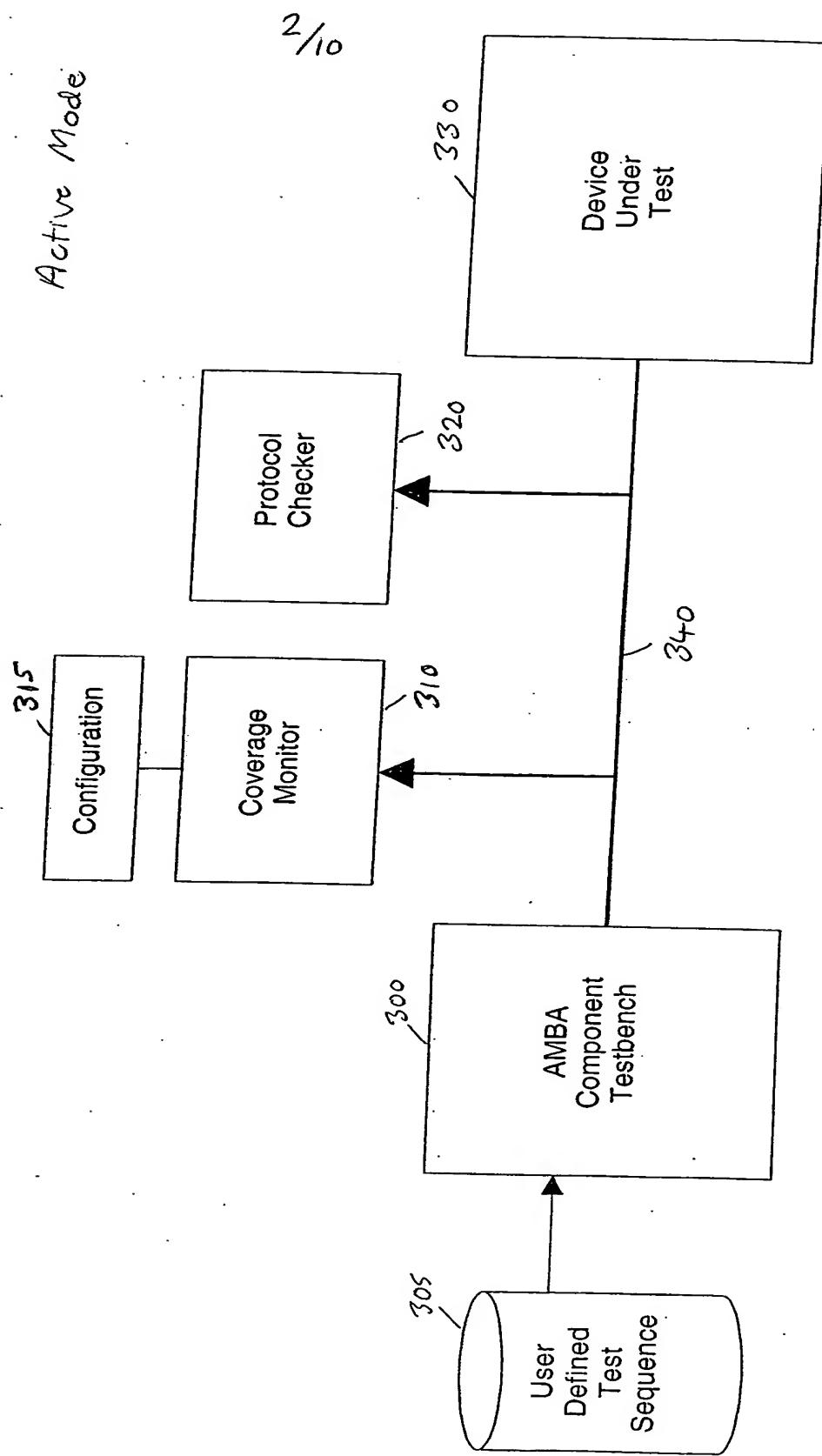


FIG. 2

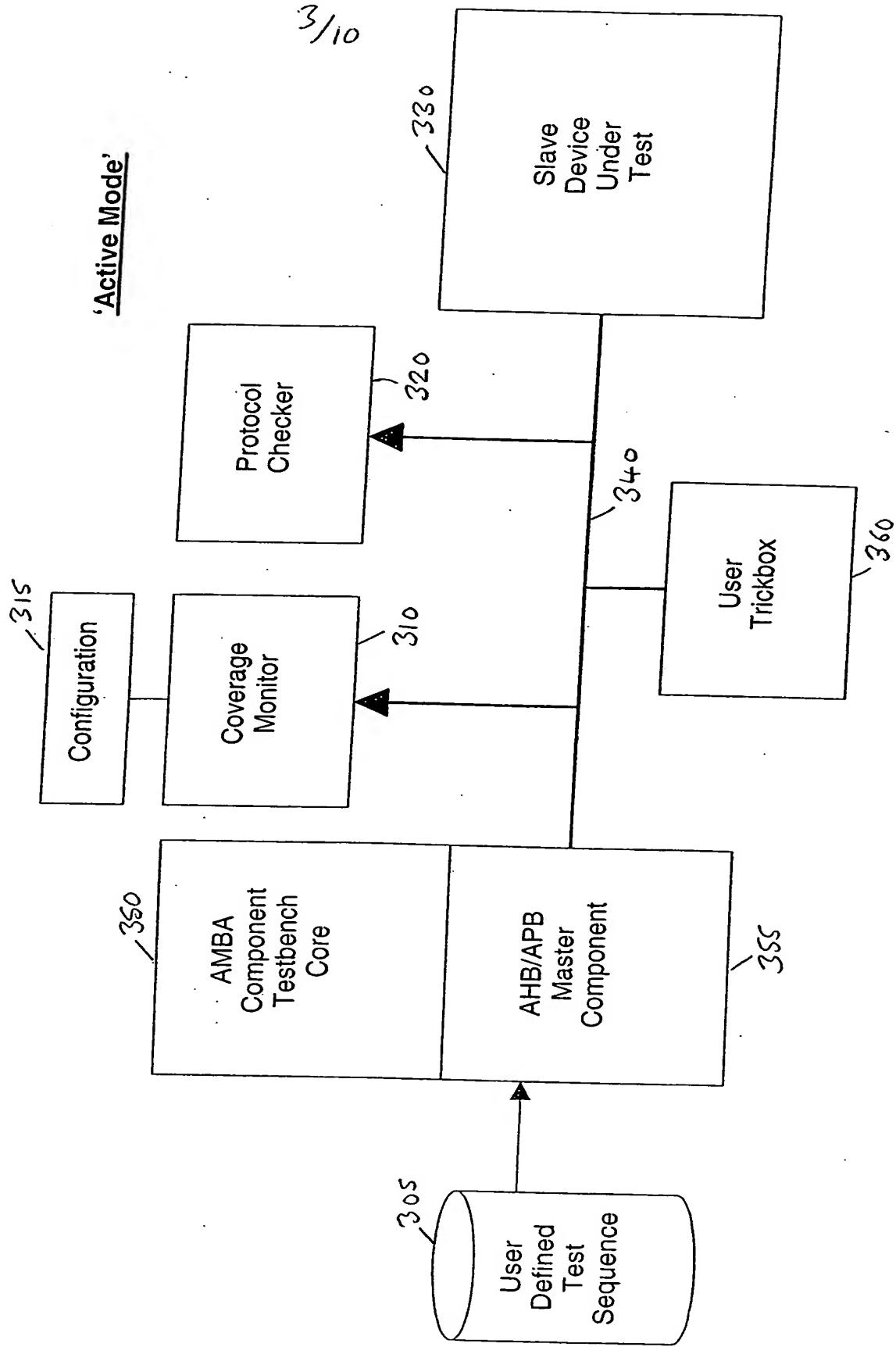


Fig. 3

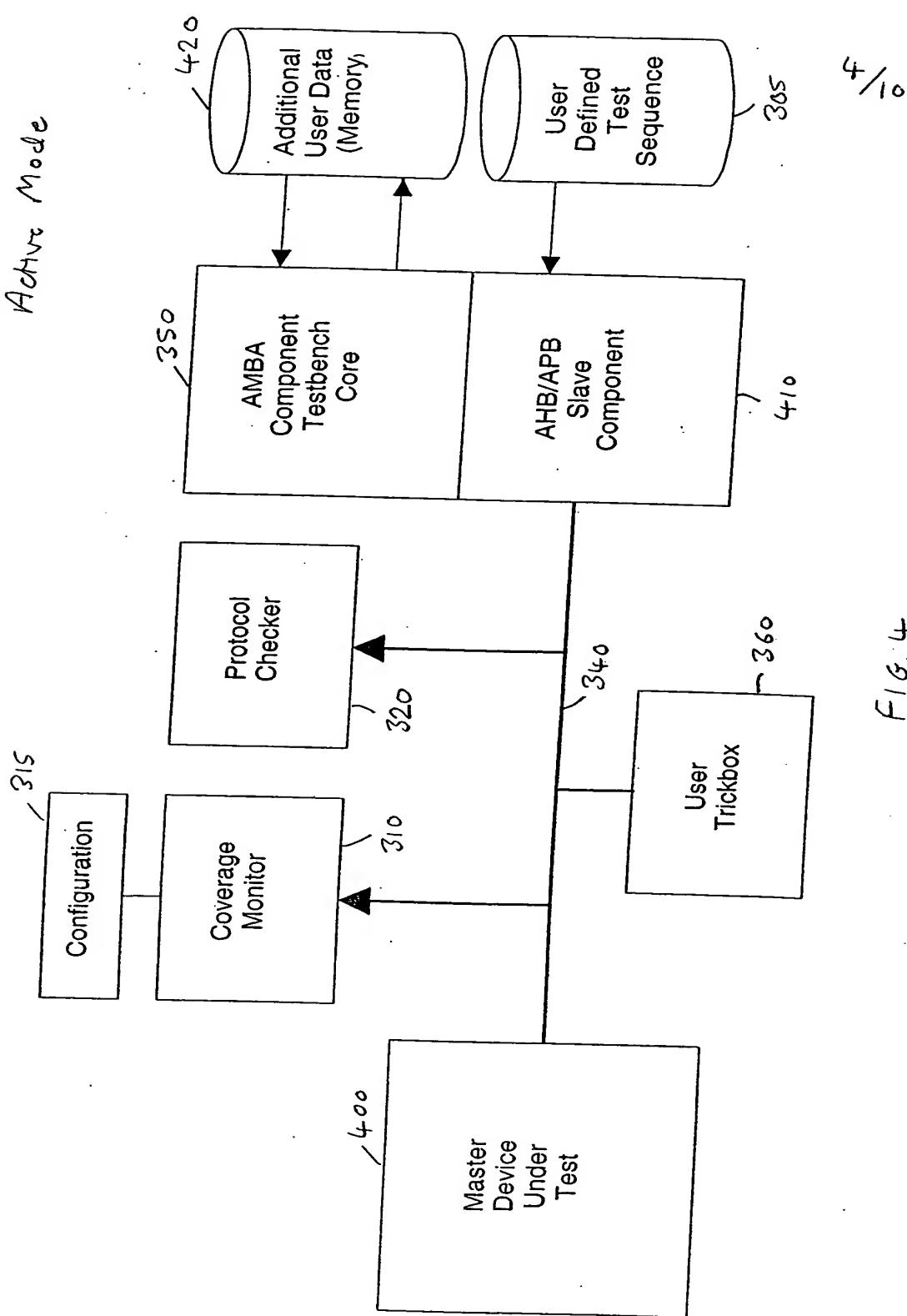


FIG. 4

'Passive Mode'

System Simulation Environment

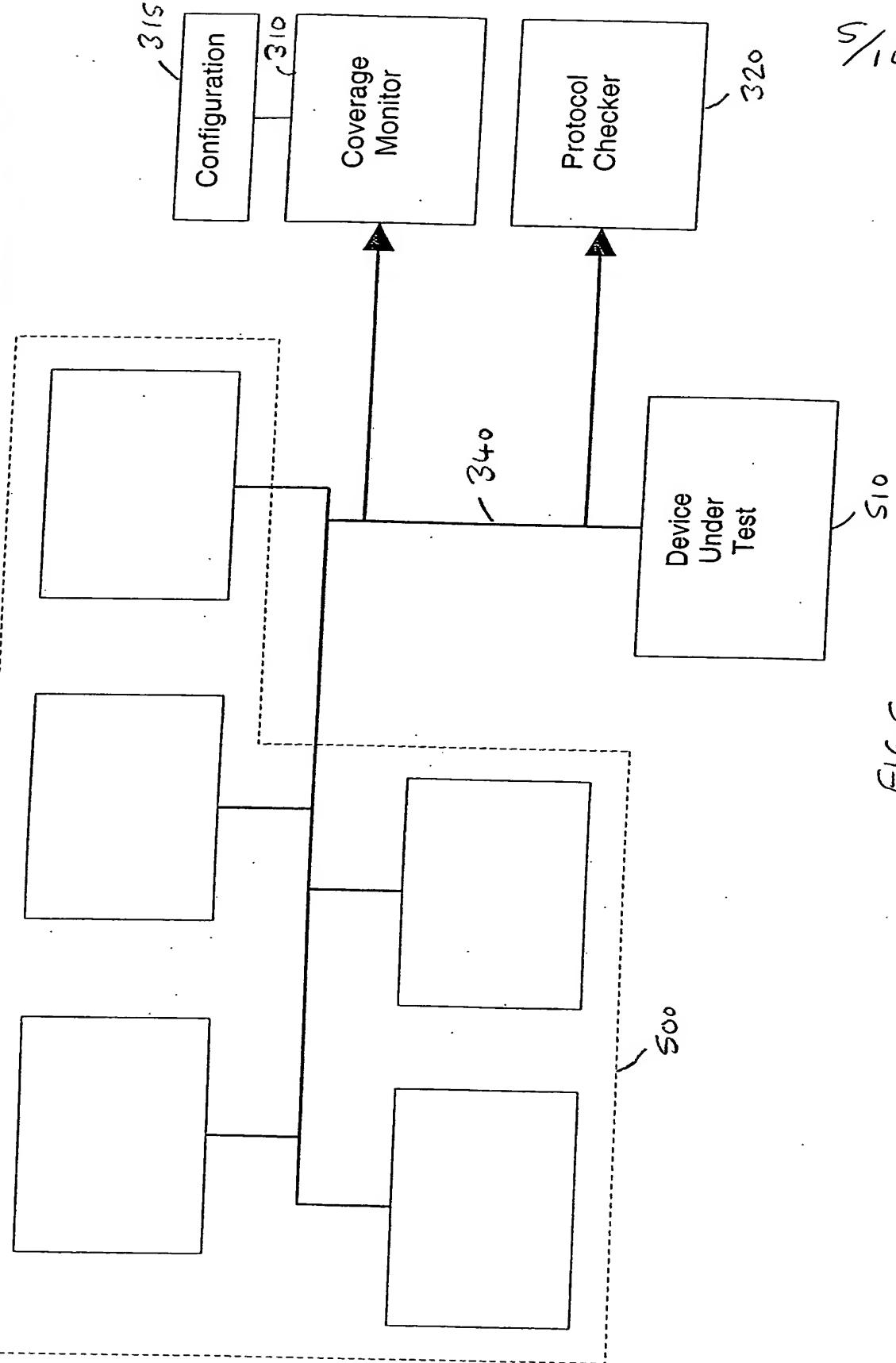


FIG. 5

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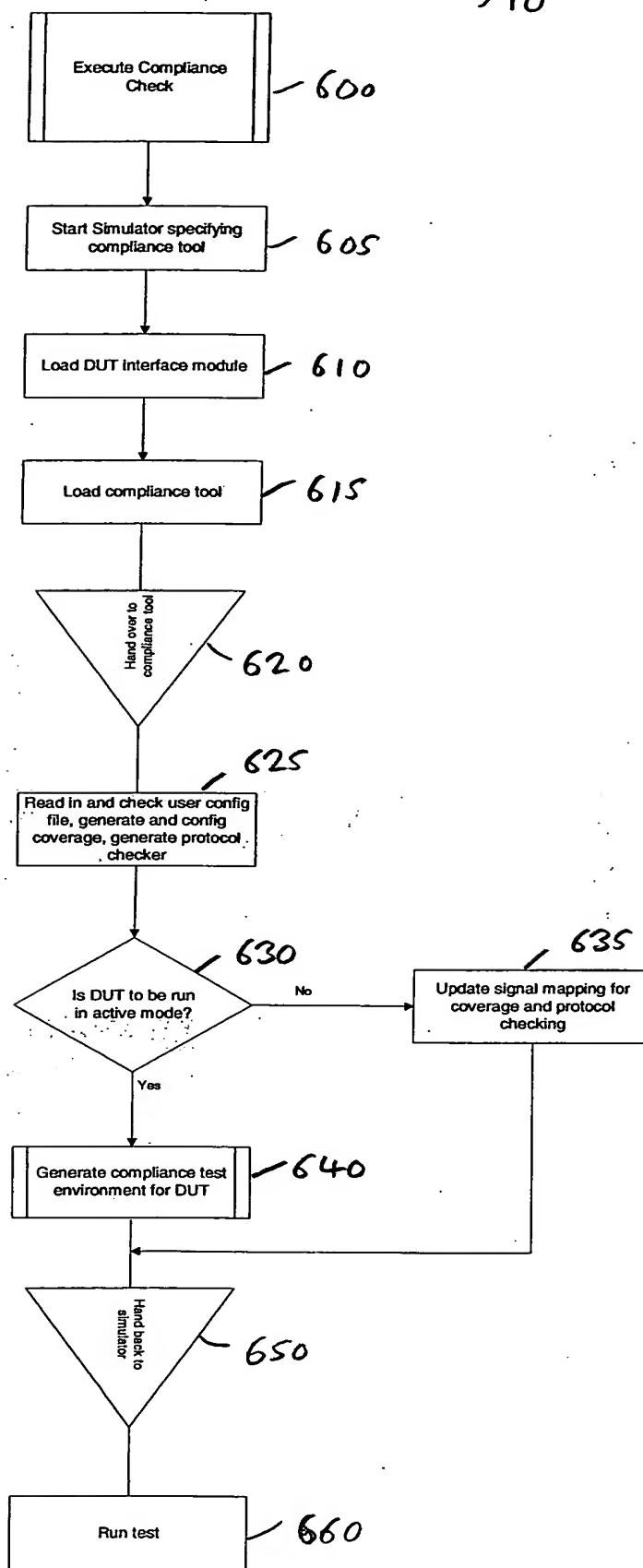


FIG. 6

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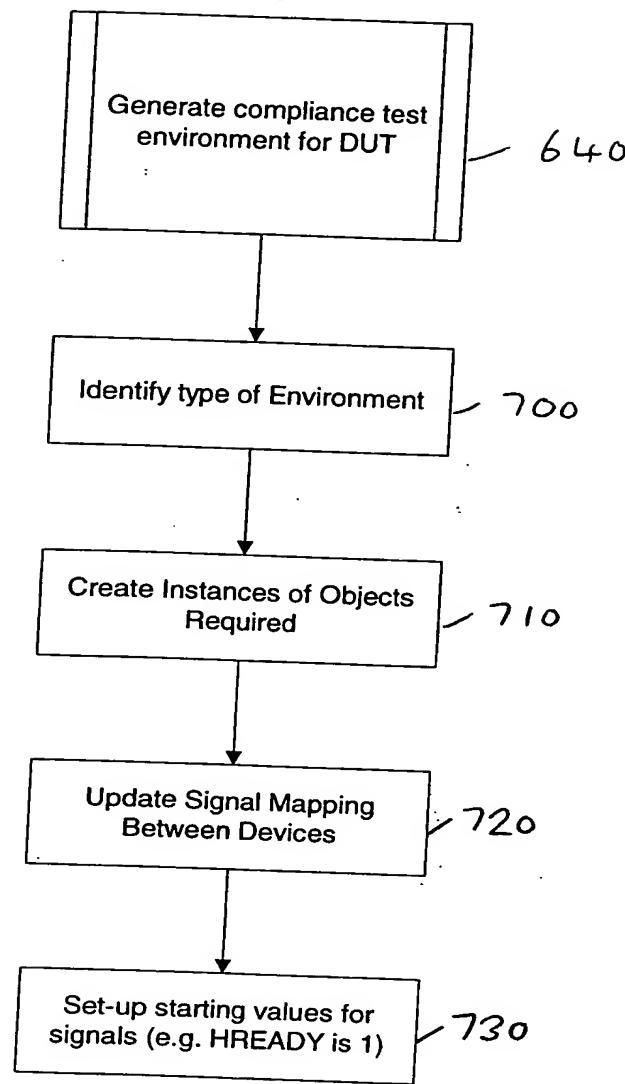
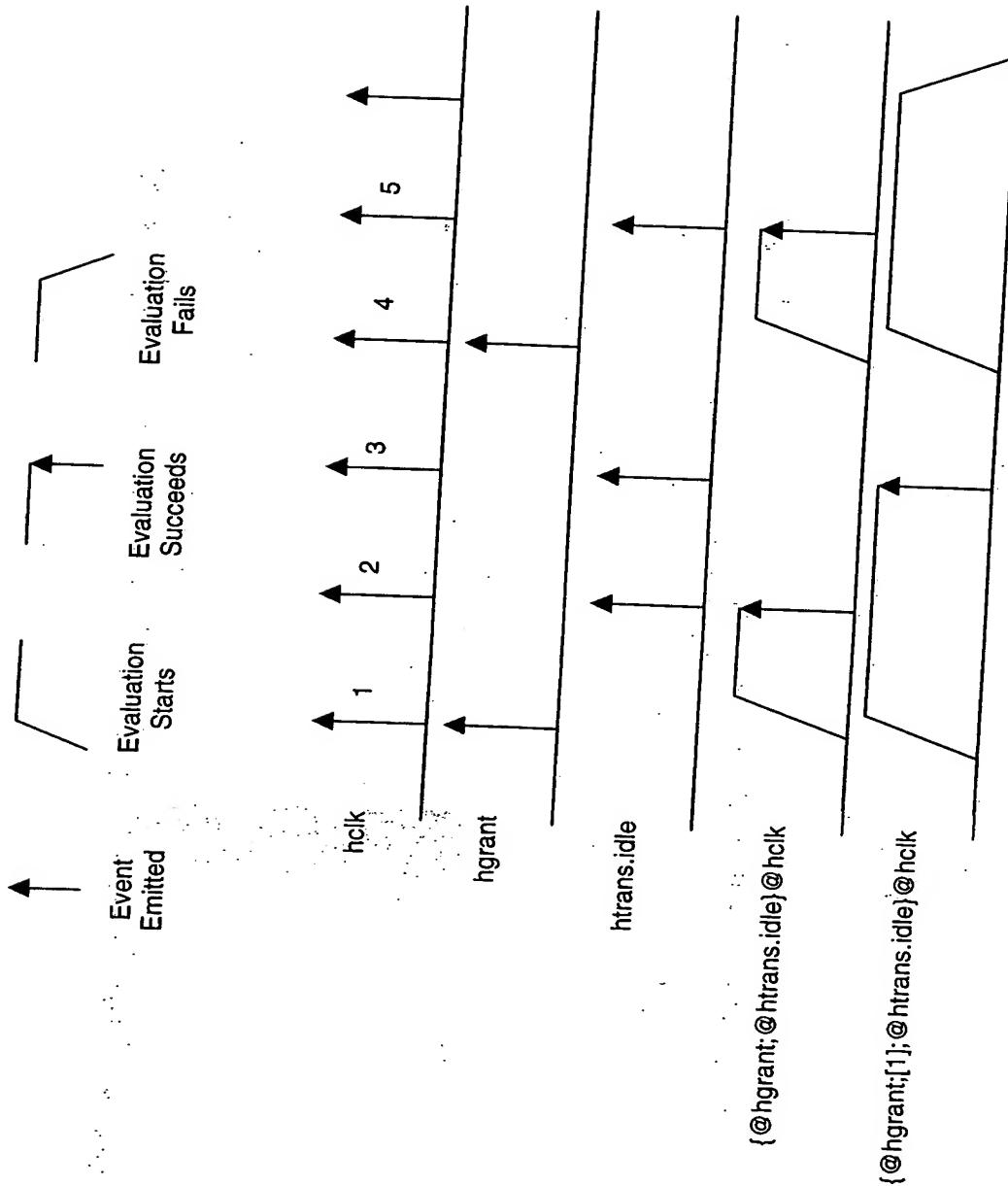


FIG. 7

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F16. 8

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Fig. 9A

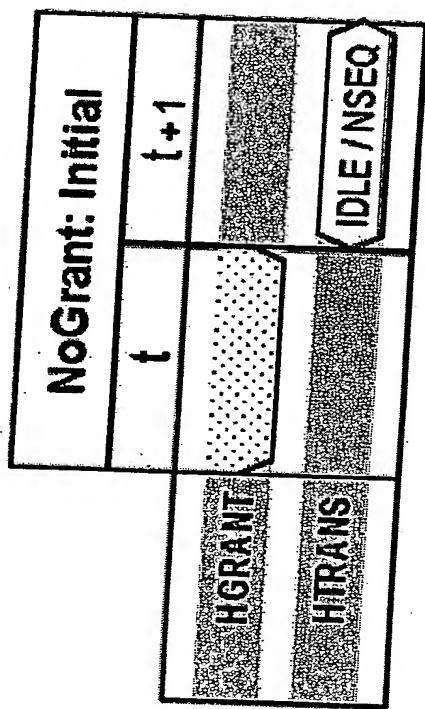
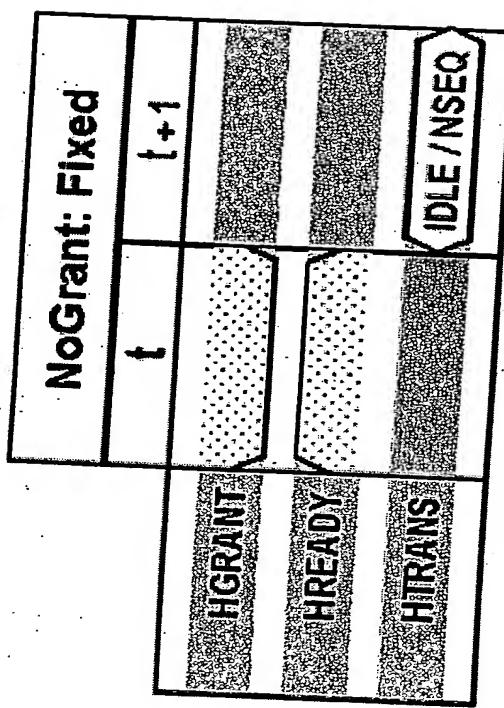


Fig. 9B



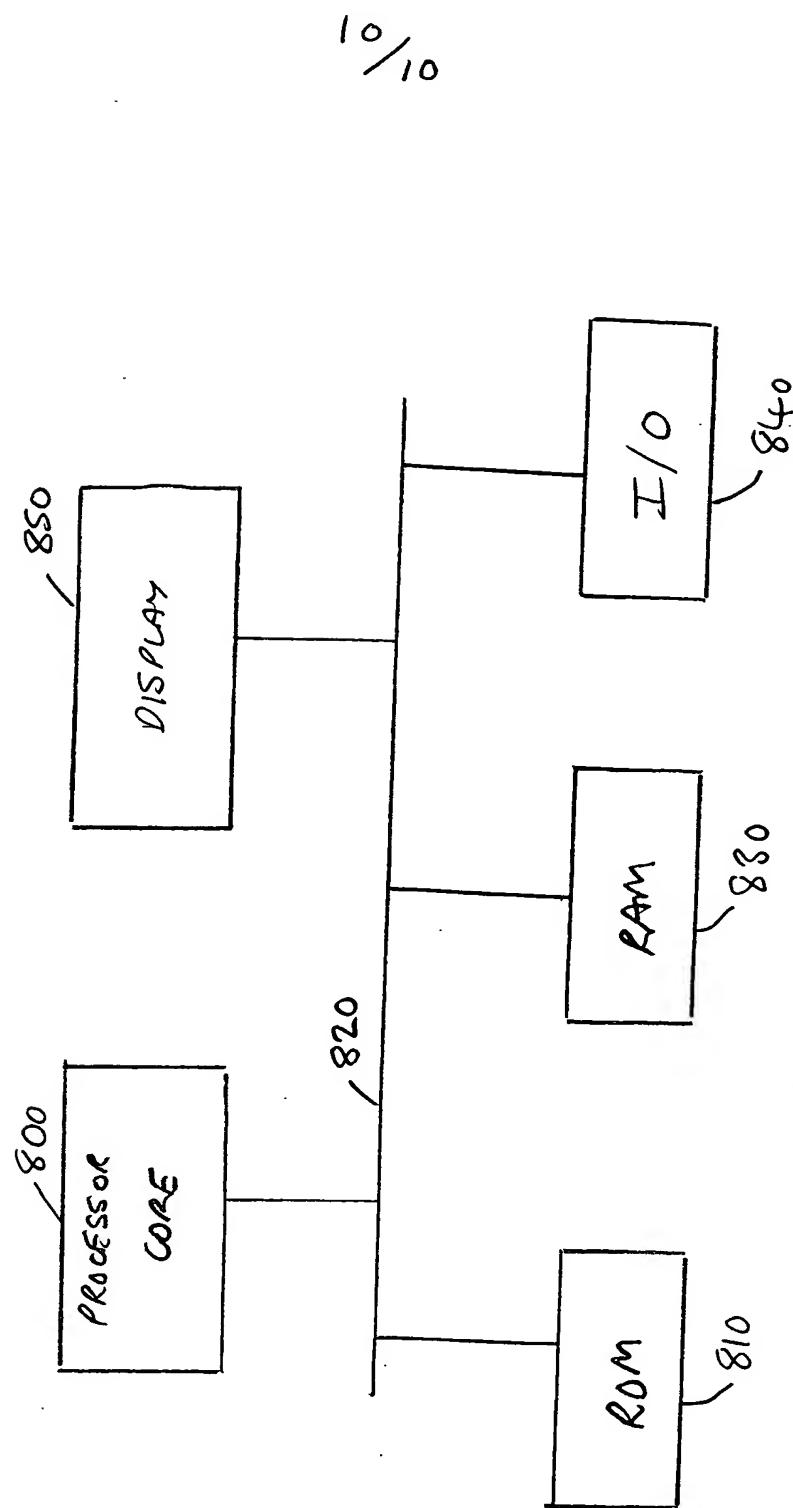


FIG. 10